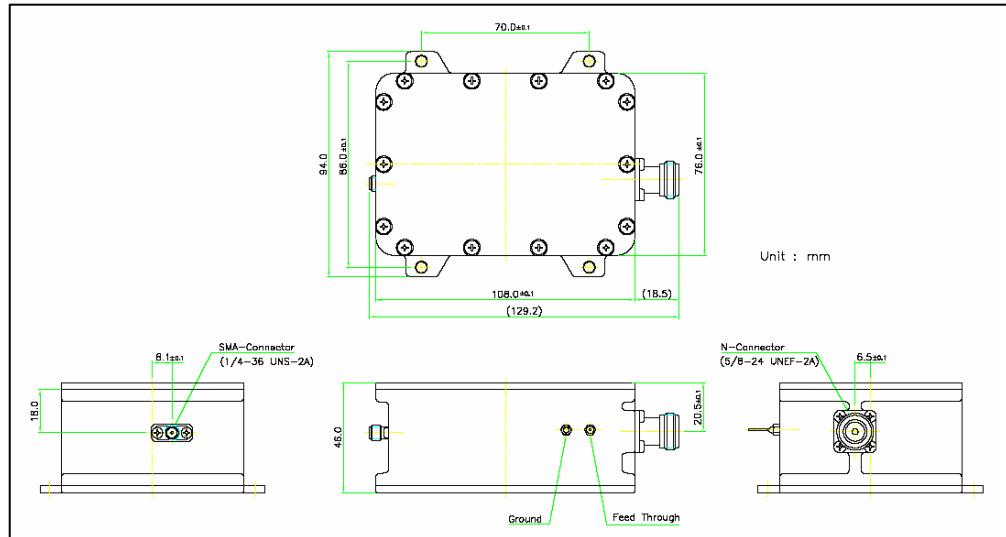


# C-Band PLL BDC (Block Down Converter ) Applications

## SM-250

### Outline Drawing



### Model No & Frequency, Stability

Model No	Input Freq.(GHz)	Output Freq.(MHz)	L.O freq. & Stab.
SM-250	3.4 – 4.2	950 – 1750	5.15GHz +/-25KHz

### Specifications

Parameter	Specification
Noise figure (@+25'C)	10 dB max.
Input VSWR	2.2 : 1 max.
Image rejection	45 dBc min.
Conversion gain	40dB typ. 38dB min. 42dB max.
Output VSWR	2.2 : 1 max.
Gain flatness	4 dBp-p max.
Gain ripple (@per 27 MHz segment)	1 dB max.
1dB gain compression point	+5 dBm min.
Output impedance	50, 75 ohms
Leakage (@ input port)	-45 dBm max.
Phase noise	-73 dBc/Hz @1KHz offset -83 dBc/Hz @10KHz offset -93 dBc/Hz @100KHz offset
Power supply	+12 to +24V DC
Required current	300 mA max.
Operating temperature	-40'C to +60'C
Storage temperature	-40'C to +80'C
Relative humidity	0% to 95% RH
Input connector	SMA female
IF output connector	N-type female, F-type female
Weight	500 g



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